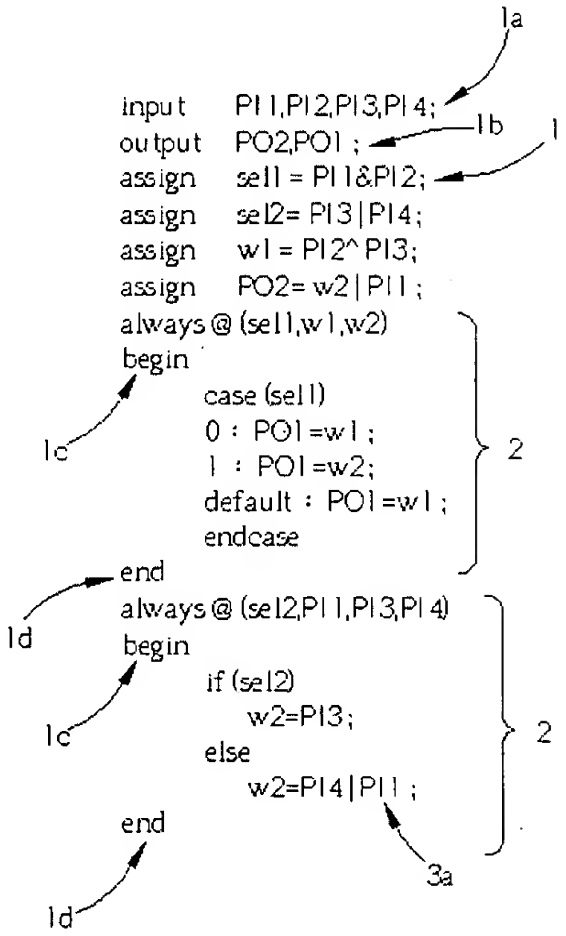
1a
input PI1,PI2,PI3,PI4;
1b
output PO2,PO1 ;
1
assign sel1 = PI1&PI2;
assign sel2= PI3|PI4;
assign w1 = PI2^PI3;
assign PO2= w2|PI1;
always @ (sel1,w1,w2)
begin
1c
case (sel1)
0 : PO1=w1;
1 : PO1=w2;
default : PO1=w1;
endcase
2
end
1d
always @ (sel2,PI1,PI3,PI4)
begin
1c
if (sel2)
w2=PI3;
else
w2=PI4|PI1;
2
end
3a
1d

Fig. 1

```
input    PI1,PI2,PI3,PI4;
output   PO2,PO1;                                    20
assign   sel1 = PI1&PI2;
assign   sel2= PI3|PI4;                              21
assign   w1 = PI2^PI3;                               22
assign   PO2= w2|PI1;                                23
always@(sel1,w1,w2)                                  24
begin
      case (sel1)                                    25
      0 : PO1 =w1;                                   26
      1 : PO1 =w2;                                   27
      default : PO1 =w1;                             28
      endcase
end
always@(sel2,PI1,PI3,PI4)                            29
begin
      if (sel2)                                      30
         w2=PI3;                                     31
      else
         w2=PI4;                                     3b
end
```

Fig. 2

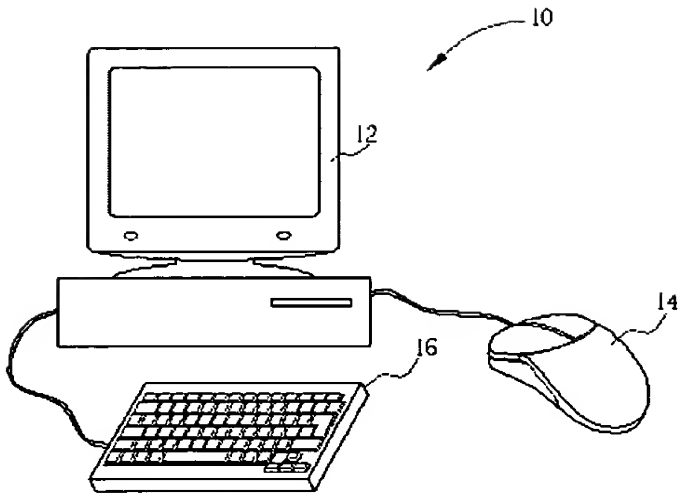

Fig. 3

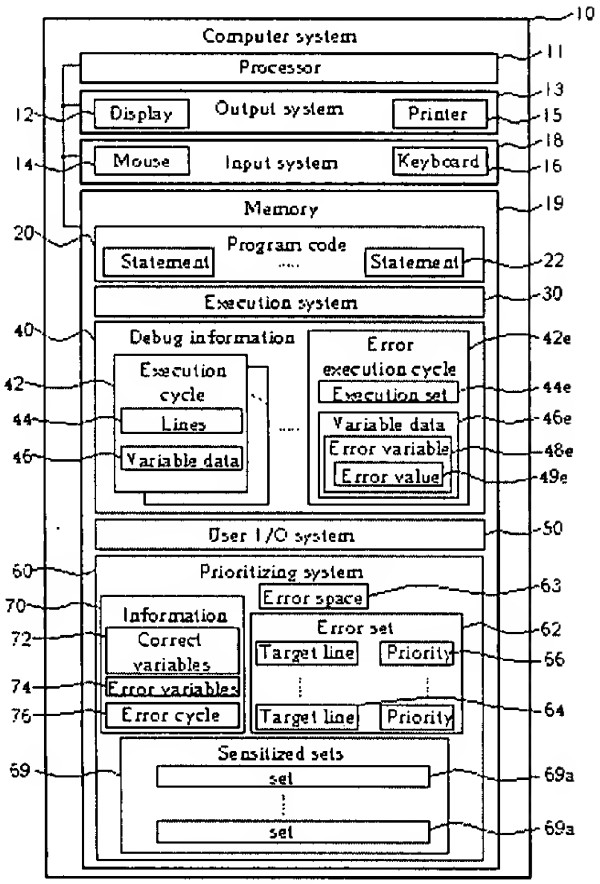
10
Computer system
11
Processor
13
12
Display
Output system
Printer
15
18
14
Mouse
Input system
Keyboard
16
19
Memory
20
Program code
Statement
.....
Statement
22
Execution system
30
40
Debug information
42e
Error
execution cycle
42
Execution
cycle
Execution set
44e
44
Lines
.....
Variable data
46e
46
Variable data
Error variable
48e
Error value
49e
User I/O system
50
60
Prioritizing system
63
Error space
70
Information
Error set
72
Correct
variables
Target line
Priority
62
56
74
Error variables
76
Error cycle
Target line
Priority
64
69
Sensitized sets
set
69a
set
69a

Fig. 4